

6

5

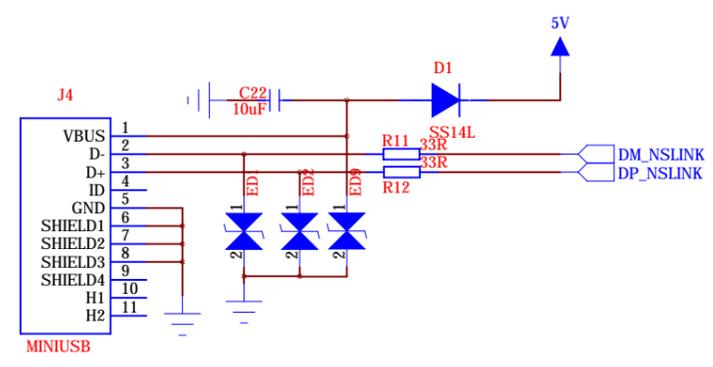
4

3

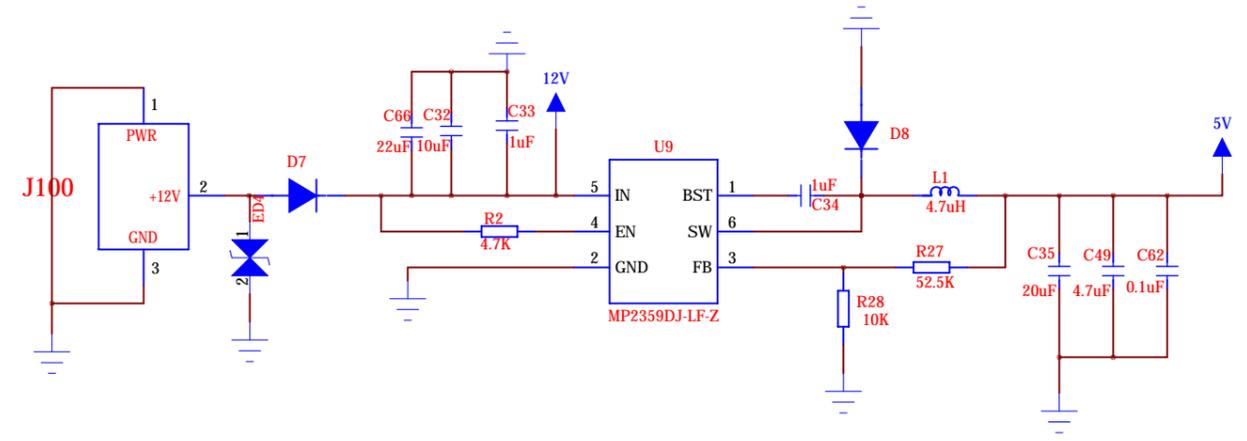
2

1

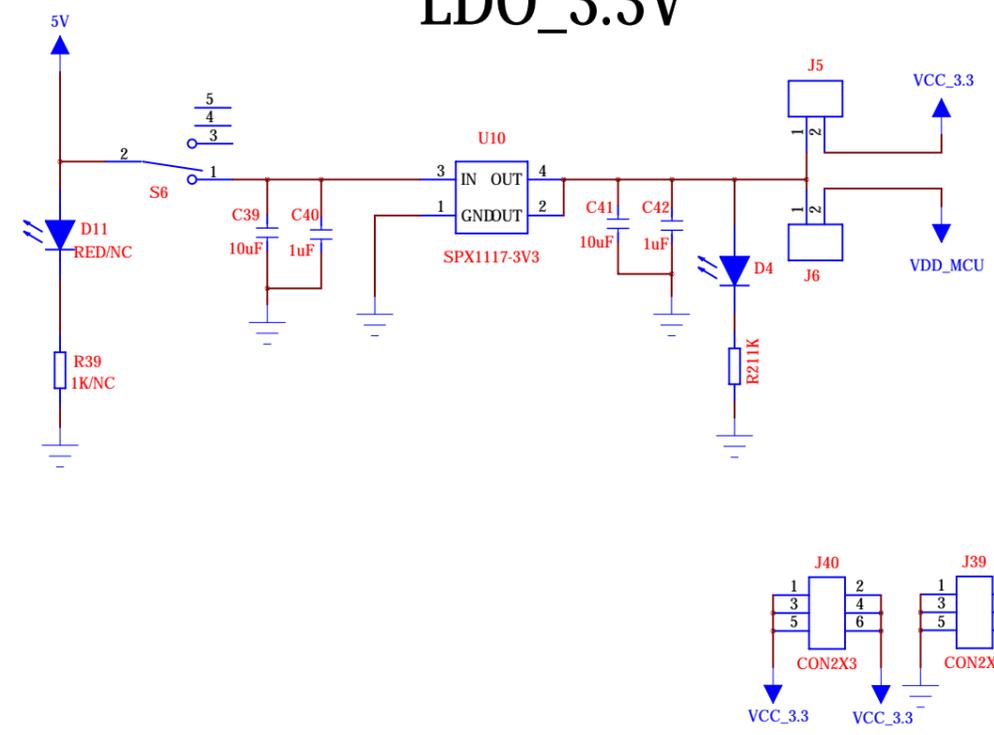
# DEBUG USB



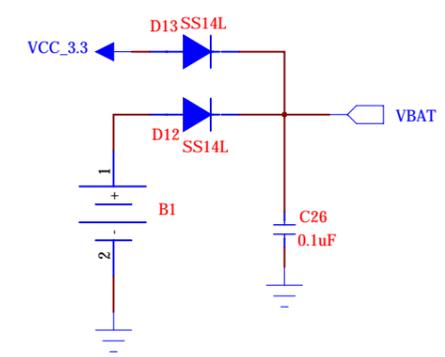
# DC-JCAK



# LDO\_3.3V

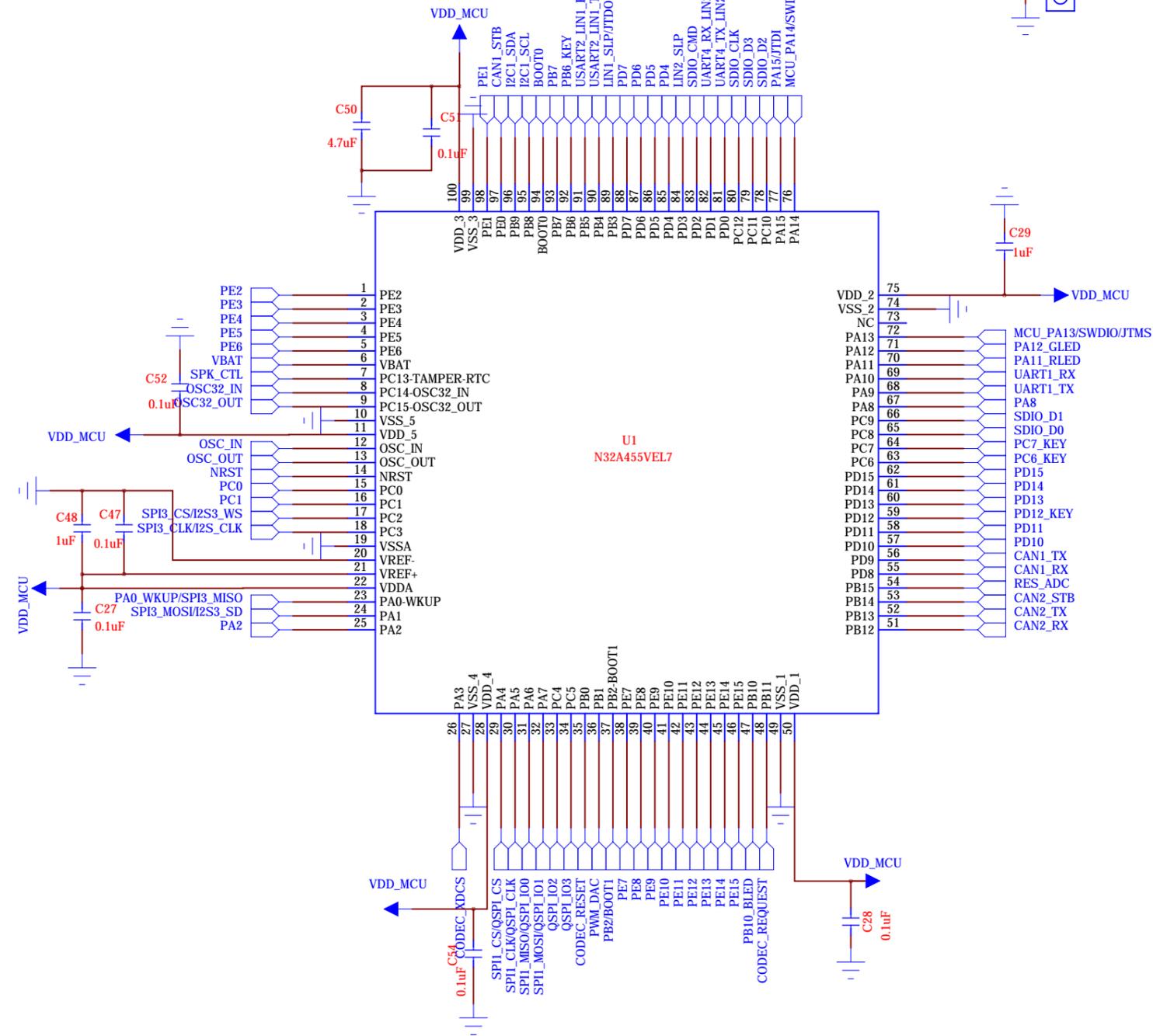


# VBAT

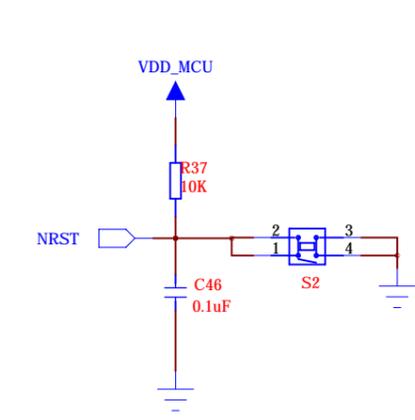


TITLE: N32A455VEL7-EVB	
SIZE: A3	REV: V1.0
DATED: 2020-06-18	SHEET: 1 OF 7

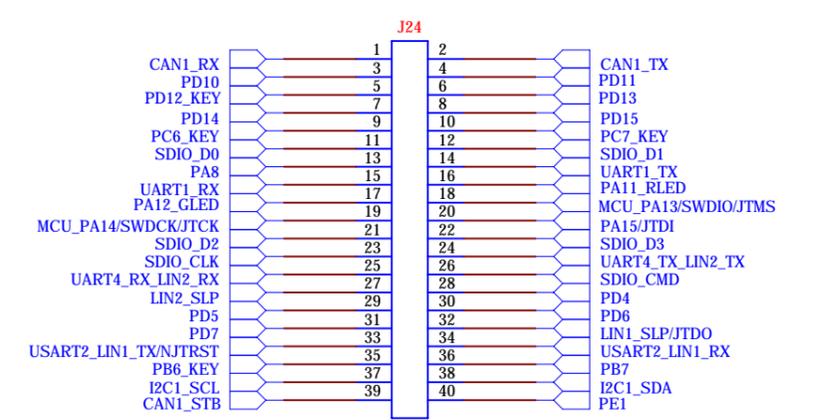
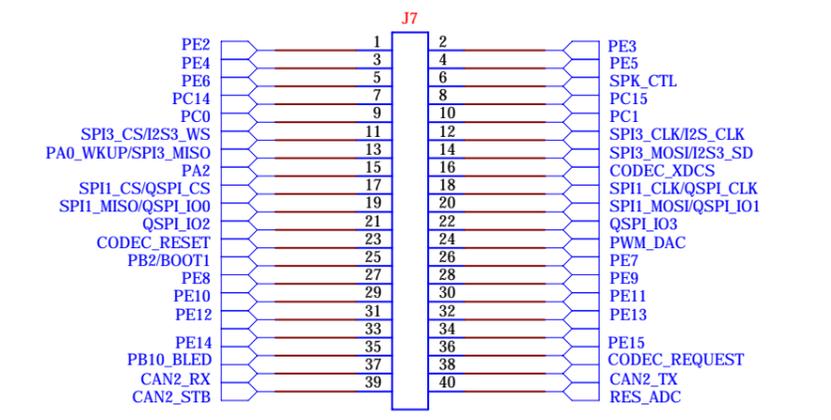
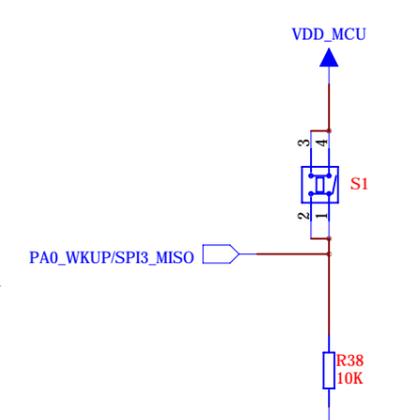
# MCU



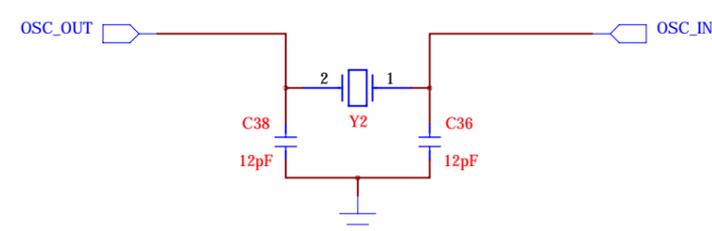
# RESET



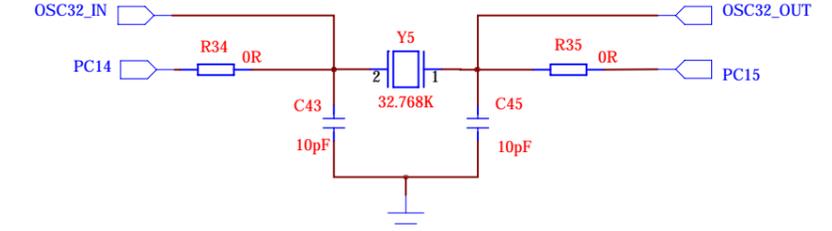
# MCU WAKEUP



# 8M Crystal



# 32.768K Crystal



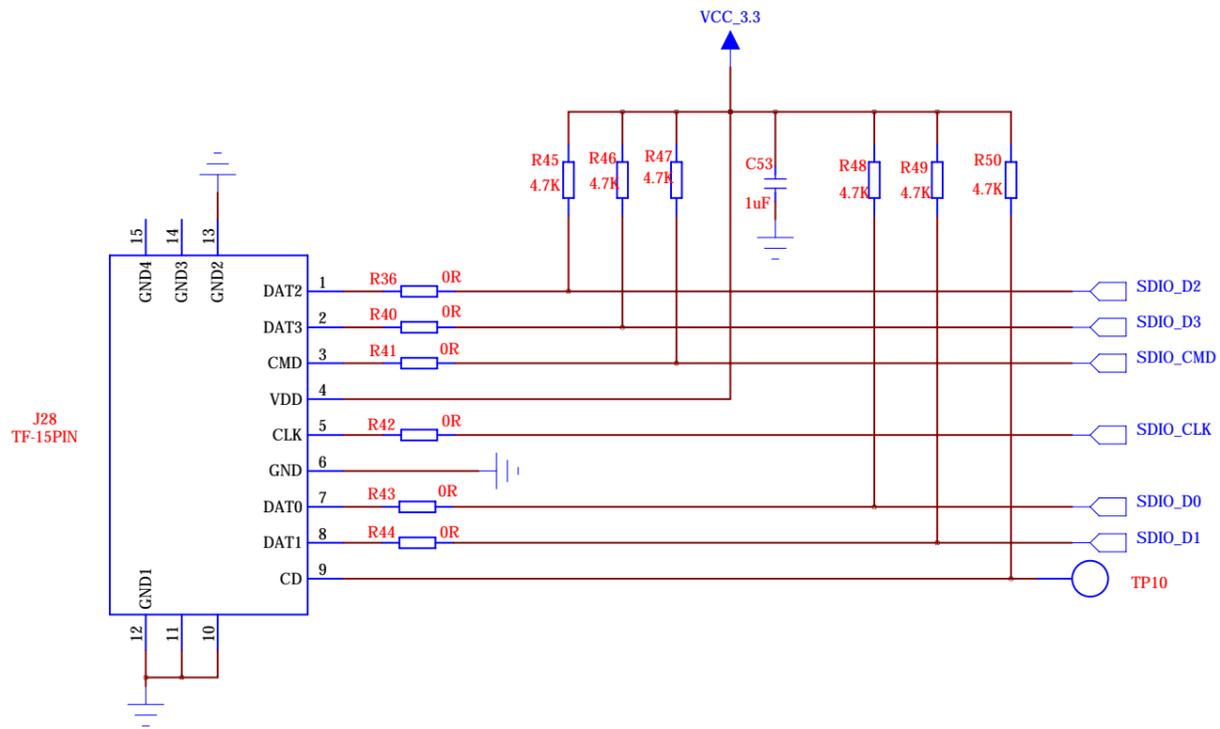
**Nation**

TITLE: N32A455VEL7-EVB

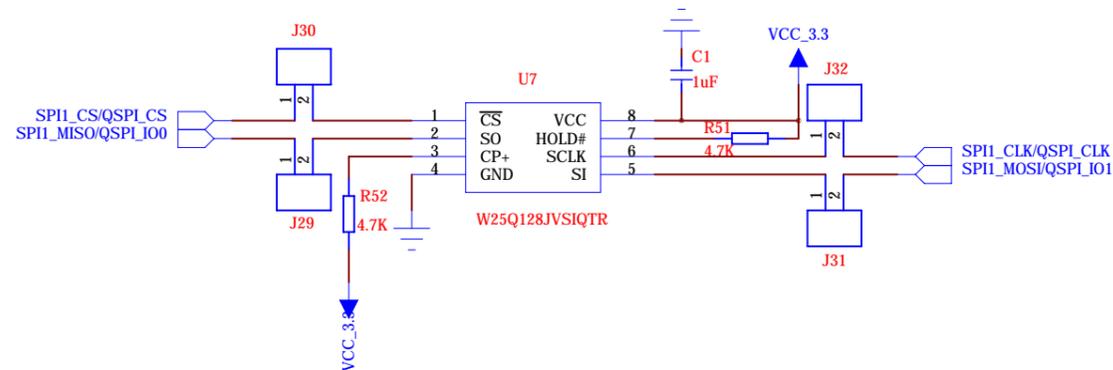
SIZE: A3      REV: V1.0

DATED: 2020-06-18      SHEET: 2 OF 7

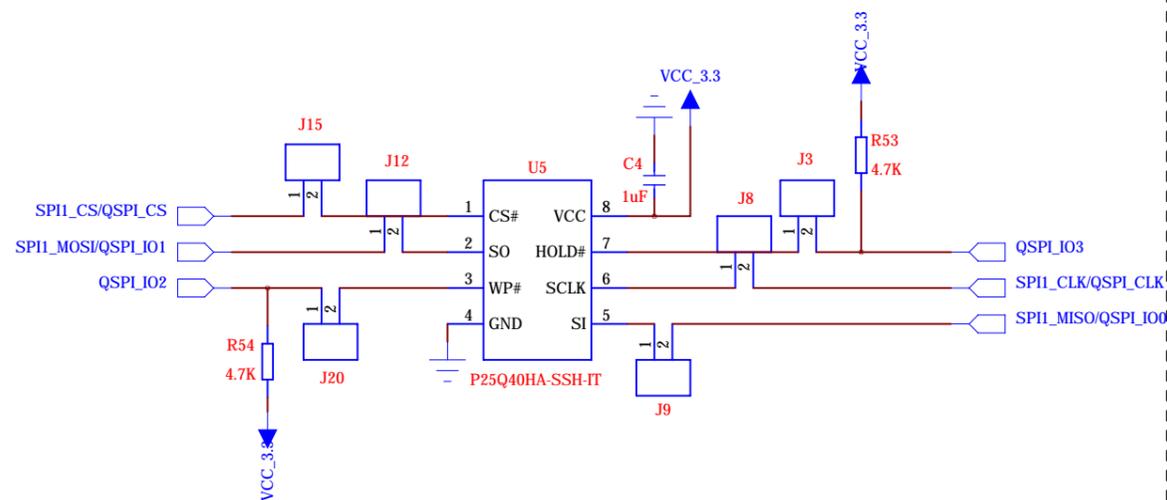
# TF1-Card



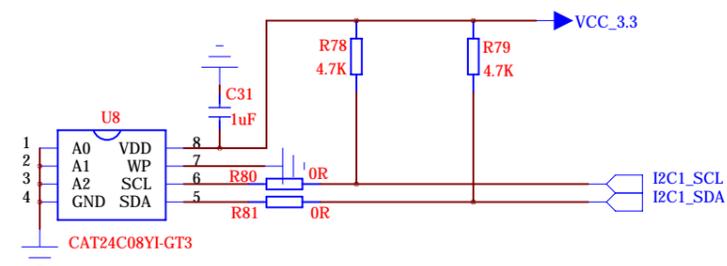
# SPI\_FLASH



# QSPI\_FLASH



# EEROM



TITLE: N32A455VEL7-EVB

SIZE: A3 REV: V1.0

DATED: 2020-06-18 SHEET: 3 OF 7

D

C

B

A

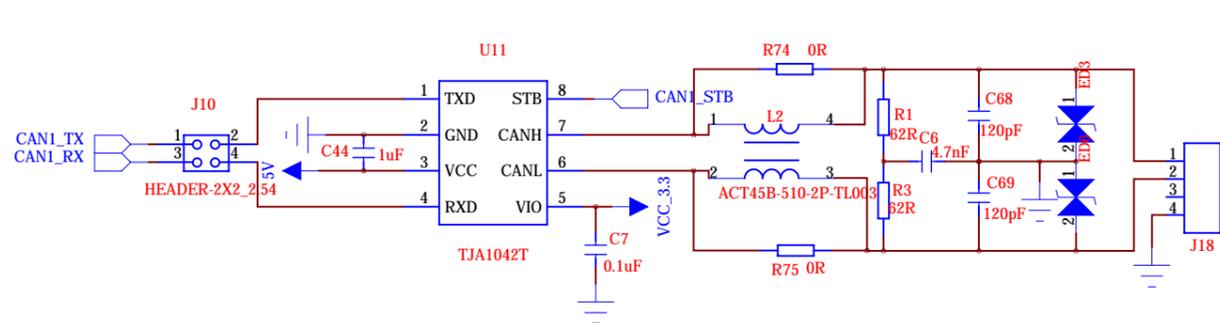
D

C

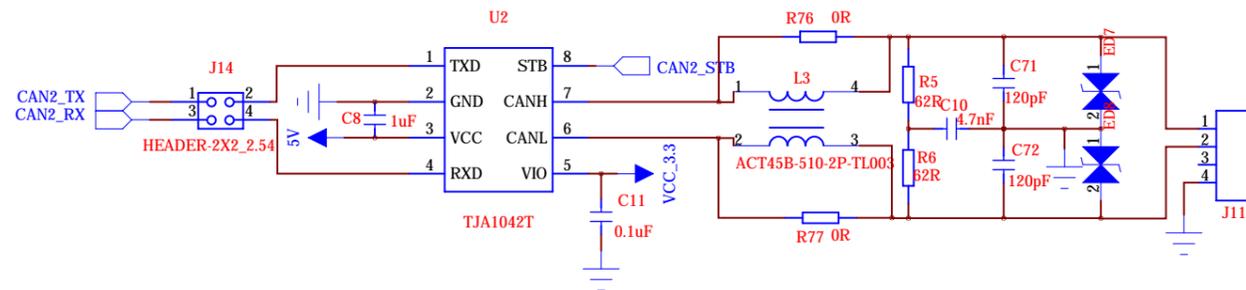
B

A

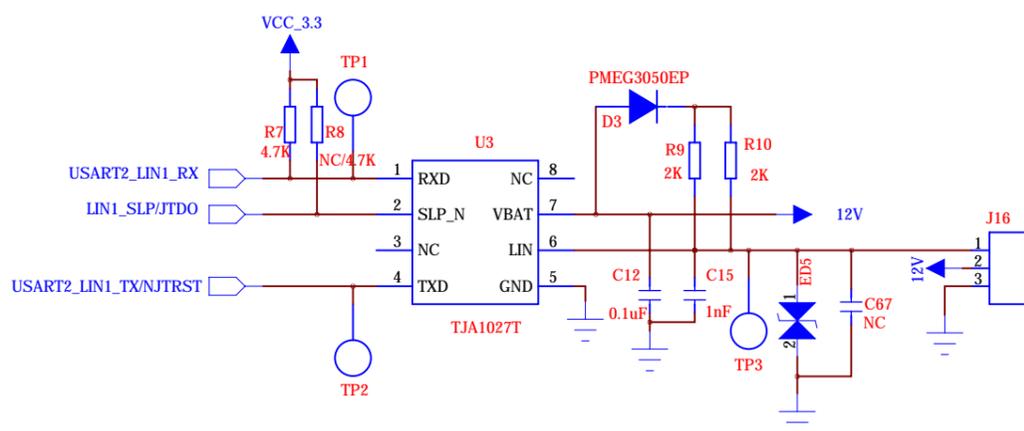
# CAN1



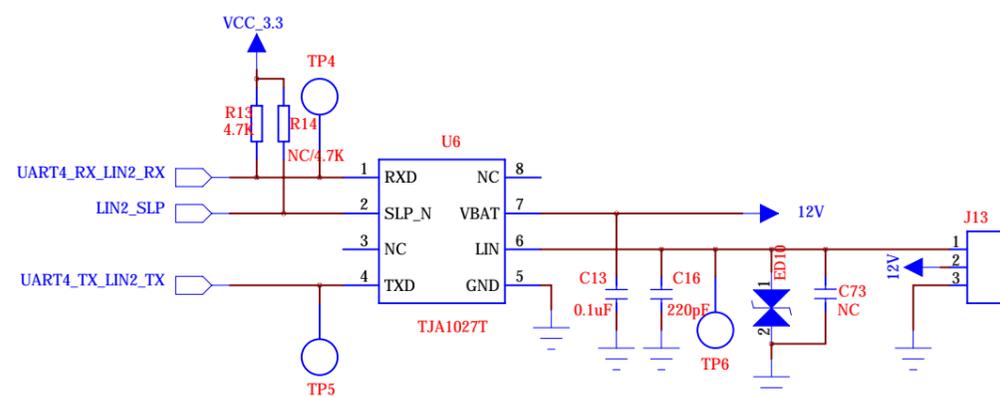
# CAN2



# MASTER\_LIN



# SLAVE\_LIN

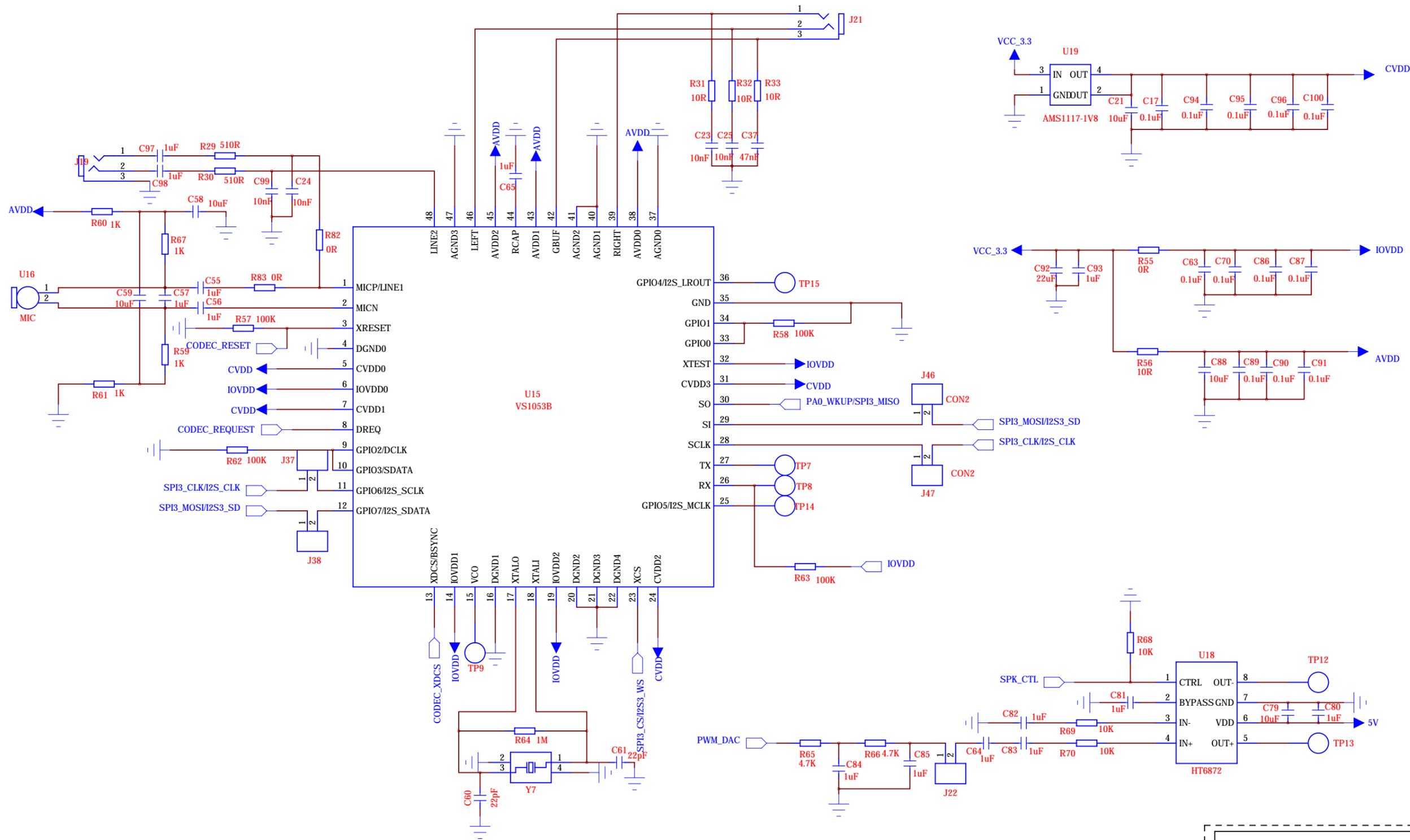


TITLE: N32A455VEL7-EVB

SIZE: A3 REV: V1.0

DATED: 2020-06-18 SHEET: 4 OF 7

# CODEC

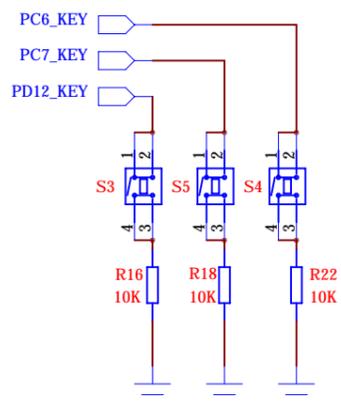


**Nation**

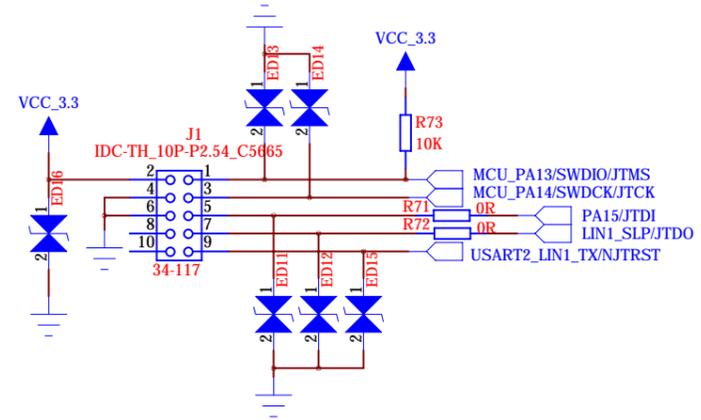
TITLE: **N32A455VEL7-EVB**

SIZE: A3	REV: V1.0
DATED: 2020-06-18	SHEET: 5 OF 7

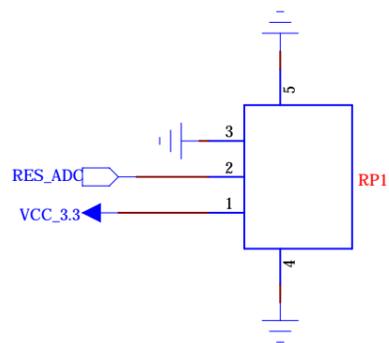
### KEY



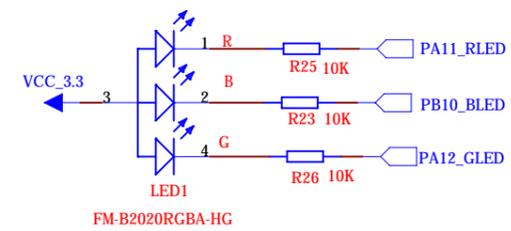
### JTAG



### ADC



### LED



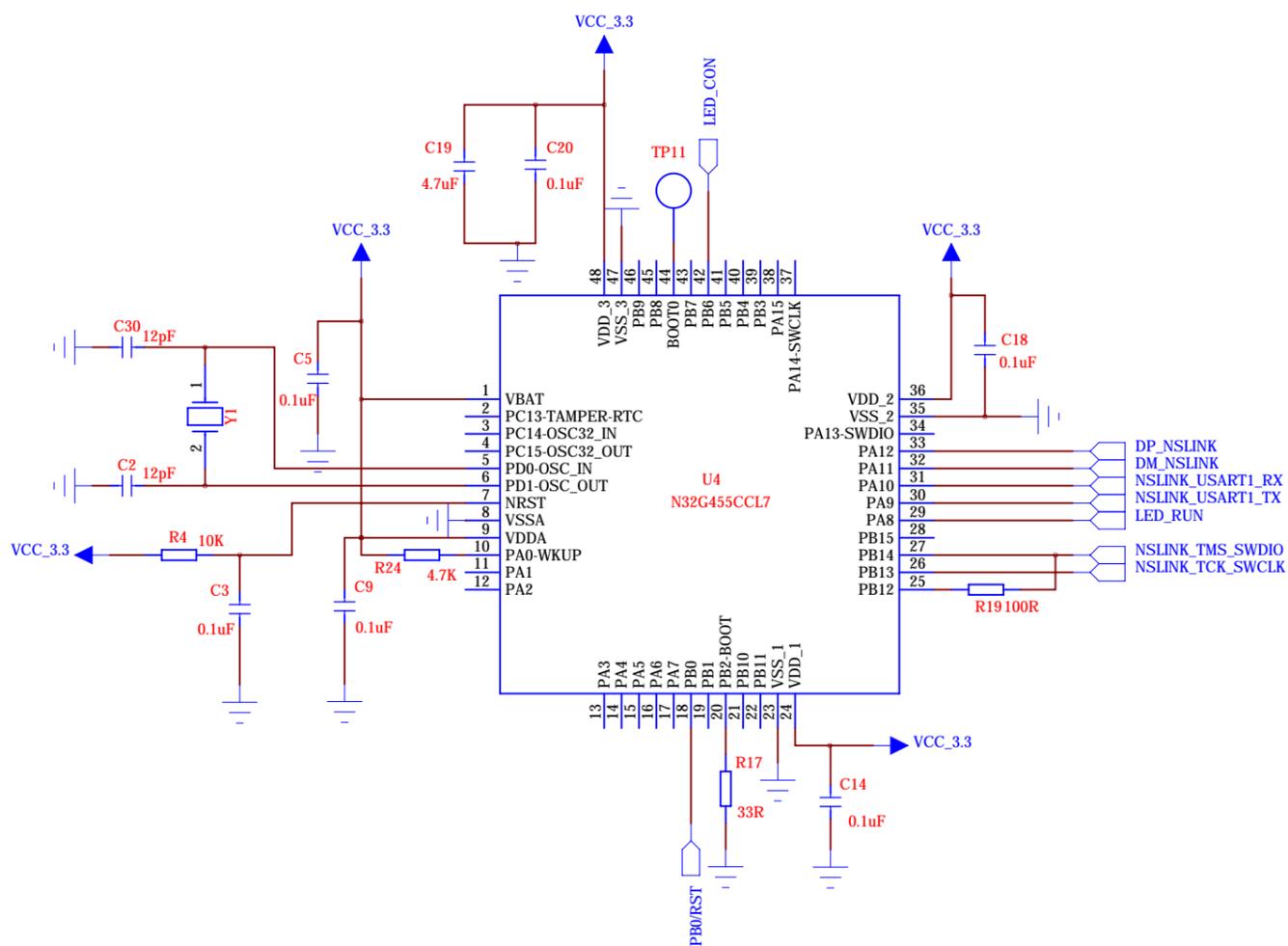
**Nation** 

TITLE: **N32A455VEL7-EVB**

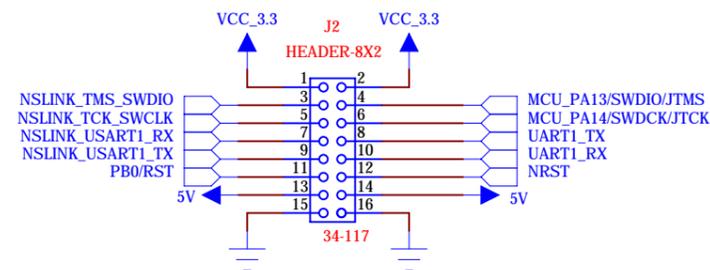
SIZE: A3	REV: V1.0
DATED: 2020-06-18	SHEET: 6 OF 7

# NS-LINK

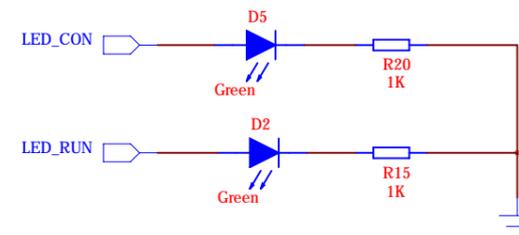
## MCU



## HEADER



## LED



TITLE: **N32A455VEL7-EVB**

SIZE: **A3**      REV: **V1.0**

DATED: **2020-06-18**      SHEET: **7 OF 7**