

Application note

N32G430 series HSI Trim application note

Introduction

In practical product applications, HSI (High-Speed Internal) clocks are frequently used. However, when the accuracy of the HSI clock is insufficient, if left unaddressed, some modules may fail to function properly. In such cases, calibration of the HSI becomes necessary.

This document outlines the process of calibrating the HSI using a HSE (High-Speed External) crystal or a LSE (Low-Speed External) crystal, aiming to adjust the HSI frequency to an appropriate range.

This document applies to the N32G430 series of Nations Technologies.

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1. HSI TRIM introduction

The HSI (High Speed Internal) clock signal is generated by an internal 8MHz RC oscillator, which can be directly used as the system clock or divided by 2 to serve as the input for the PLL. The HSI RC oscillator offers the convenience of providing a system clock without requiring any external components. Its startup time is shorter than HSE (High Speed External) crystal oscillator. Nevertheless, due to its relatively poor frequency accuracy, calibration is necessary for proper operation.

This document introduces the calibration of the HSI (High Speed Internal) clock using either the HSE (High Speed External) clock or the LSE (Low Speed External) clock.

2. Instructions for Using HSI TRIM

In the application note project, the macro definitions "HSI_TRIM_BY_HSE" and "HSI_TRIM_BY_LSE" are used to select whether HSE or LSE will be employed for HSI calibration.

If HSE is chosen for HSI calibration, the HSI_Triming_By_HSE function is utilized to capture the HSE/128 frequency using TIM2 (Timer Input Capture), subsequently calibrating the HSI. The default HSE frequency is 8MHz.

If LSE is selected for HSI calibration, the HSI_Triming_By_LSE function is utilized to capture the LSE frequency using TIM2 (Timer Input Capture), subsequently calibrating the HSI. The default LSE frequency is 32.768KHz.

Additionally, within the application note project, a configuration is set up to output the HSI frequency through pin PA8, allowing for the capture of the HSI frequency using an oscilloscope. If HSE or LSE fails to start, or if the calibration attempts exceed a predetermined limit, the function will return a failure status.

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46

Options for Target 'N32G430'

X

47

Device | Target | Output | Listing | User | C/C++ | Asm | Linker | Debug | Utilities

48

Preprocessor Symbols

49

Define: N32G430, USE_STDPERIPH_DRIVER, HSI_TRIM_BY_HSE

50

Undefine:

51

Language / Code Generation

52

☐ Execute-only Code ☐ Strict ANSI C Warnings: All Warnings

53

Optimization: Level 0 (-O0) ☐ Enum Container always int ☐ Thumb Mode

54

☐ Optimize for Time ☐ Plain Char is Signed ☐ No Auto Includes

55

☐ Split Load and Store Multiple ☐ Read-Only Position Independent ☐ C99 Mode

56

☒ One ELF Section per Function ☐ Read-Write Position Independent ☐ GNU extensions

57

Include Paths: ..\..\..\..\..\firmware\n32g430_std_periph_driver\inc;..\..\..\..\..\firmware\CMSIS\core;..\..\..\..\..\

58

Misc Controls:

59

Compiler control string: -c -cpu Cortex-M4 -D __MICROLIB -g -O0 -apcs=interwork -split_sections -l ..\..\..\..\..\firmware\n32g430_std_periph_driver\inc -l ..\..\..\..\..\firmware\CMSIS\core -l

60

OK Cancel Defaults Help

61

GPIO_InitStructure.GPIO_Alternate = GPIO_NO_AF;

62

GPIO_Peripheral_Initialize(GPIOA, &GPIO_InitStructure);

63

GPIO_InitStructure.Pin = GPIO_PIN_8;

64

GPIO_InitStructure.GPIO_Mode = GPIO_MODE_AF_PP;

65

GPIO_InitStructure.GPIO_Alternate = GPIO_AF9_MCO;

66

GPIO_Peripheral_Initialize(GPIOA, &GPIO_InitStructure);

67

RCC_MCO_Source_Config(RCC_CFG_MCO_HSI);

68

GPIO_Pins_Set(GPIOA, GPIO_PIN_7);

69

#ifdef HSI_TRIM_BY_HSE

70

result = HSI_Trimming_By_HSE();

71

#endif

72

#ifdef HSI_TRIM_BY_LSE

73

result = HSI_Trimming_By_LSE();

74

#endif

75

if(result == FAILED)

76

{

77

GPIO_Pins_Reset(GPIOA, GPIO_PIN_7);

78

log_info("HSI-TRIM-test-failed\r\n");

79

}

80

else

81

{

82

GPIO_Pins_Reset(GPIOA, GPIO_PIN_7);

83

log_info("HSI-TRIM-test-passed\r\n");

84

}

85

while(1)

86

{

87

88

}

89

}

2

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2.1 HSI TRIM Procedure

The HSI calibration process using HSE follows these steps:

1. Configure the system clock source to HSI_PLL.
2. Enable the HSE clock source and wait for it to stabilize. If a timeout occurs, return a failure.
3. Wait for a period to ensure that the HSE clock source is running stably.
4. Configure TIM2 to capture and calculate the HSE/128 frequency through TIM2 Channel 4.
5. Adjust the HSI trim value based on the measured HSE/128 frequency deviation.
6. If the measured HSI frequency deviation remains significant, repeat steps 4 and 5 until the measured HSI frequency falls within the acceptable deviation range. If a timeout occurs, return a failure.
7. Upon completion of HSI calibration, exit the calibration process.

The HSI calibration process using LSE follows these steps:

1. Configure the system clock source to HSI_PLL.
2. Enable the LSE clock source and wait for it to stabilize. If a timeout occurs, return a failure.
3. Wait for a period to ensure that the LSE clock source is running stably.
4. Configure TIM2 to capture and calculate the LSE frequency through TIM2 Channel 2.
5. Adjust the HSI trim value based on the measured LSE frequency deviation.
6. If the measured HSI frequency deviation remains significant, repeat steps 4 and 5 until the measured HSI frequency falls within the acceptable deviation range. If a timeout occurs, return a failure.
7. Upon completion of HSI calibration, exit the calibration process.

3. Version history

Version	Date	Modify
V1.0.0	2023-11-12	Create a document

4. Notice

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